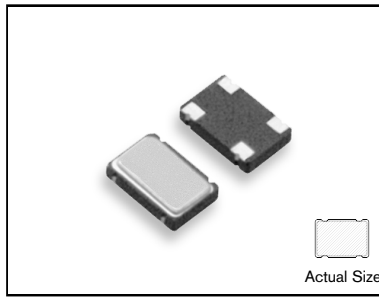


FMOC1S SERIES

1.8 Vdc Clock Oscillators

CERAMIC SMD 5x7



- Miniature, Low Profile
- 1.8 Vdc Supply Voltage
- HCMOS/TTL Compatible
- Low Current Consumption



SPECIFICATIONS

Issue 4 - 01092013

Parameter	Specification
Frequency Range	1.544 to 125 MHz
Overall Frequency Tolerance	±25 ppm to ±100 ppm (Inclusive of Operating Temp., Supply Voltage, & Load)
Operating Temperature Range	0 to +70°C Std., -40 to +85°C Avail. (See Spec. Option E)
Storage Temperature	-55 to +125°C
Supply Voltage (Vdd)	+1.8 Vdc (±0.3 Vdc)
Supply Current (Icc)	4 mA max. @ 1.544 to 35.9 MHz 7 mA max. @ 36.0 to 49.9 MHz 10 mA max. @ 50.0 to 79.9 MHz 20 mA max. @ 80.0 to 125.0 MHz
Symmetry (Duty Cycle)	40/60% Std., 45/55% Avail. (See Spec. Option S)
Output "0" Level (VOL)	10% of Vdd max.
Output "1" Level (VOH)	90% of Vdd min.
Rise and Fall Time	4 ns max. (1.544 to 35.9 MHz) 2.5 ns max. (36 to 125 MHz)
Jitter	1.5 pSec RMS max.
Start Time	10 ms max. < 5 ms typical
Output Load	2 TTL / 15 pF HCMOS
Pin 1 Options (See Spec. Option T below)	No Connect (Std.) Tri-State (option T) VIH: 2.2 V or Open Enables Output VIL: 0.8 V Disables Output Output Disable/Enable Time: 100 nS max.
Aging @ 25°C	±5ppm max first year

All specifications subject to change without notice.

STANDARD MARKING

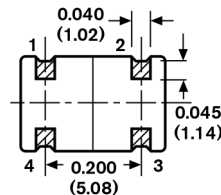
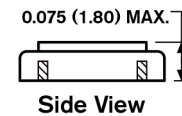
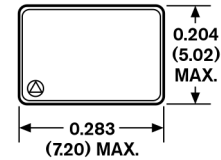
XXX.XXXM
XXXXXXXX
• FMI YYWW

XXX.XXXM FREQUENCY in MHz
XXXXXXXX Part Number
Pin 1 Symbol, FMI, Date Code

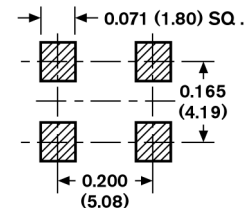
PIN FUNCTION TABLE

Pin	Function
1	NC or Tri-State
2	Case Ground
3	Output
4	Supply Voltage (Vdd)

CERAMIC SMD



Bottom View



Recommended Land Pattern

Standard Specifications for product indicated in **color**

Dimensions: Inches
(mm)

PART DESCRIPTION SYSTEM

